

Docket No. 40032/JEJ/X2

Attached hereto is a marked-up version of the changes made to the specification by the current amendment. The attached page is captioned "Version with markings to show changes made."

Respectfully submitted,

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JEJ/l11

VERSION WITH MARKINGS TO SHOW CHANGES MADE

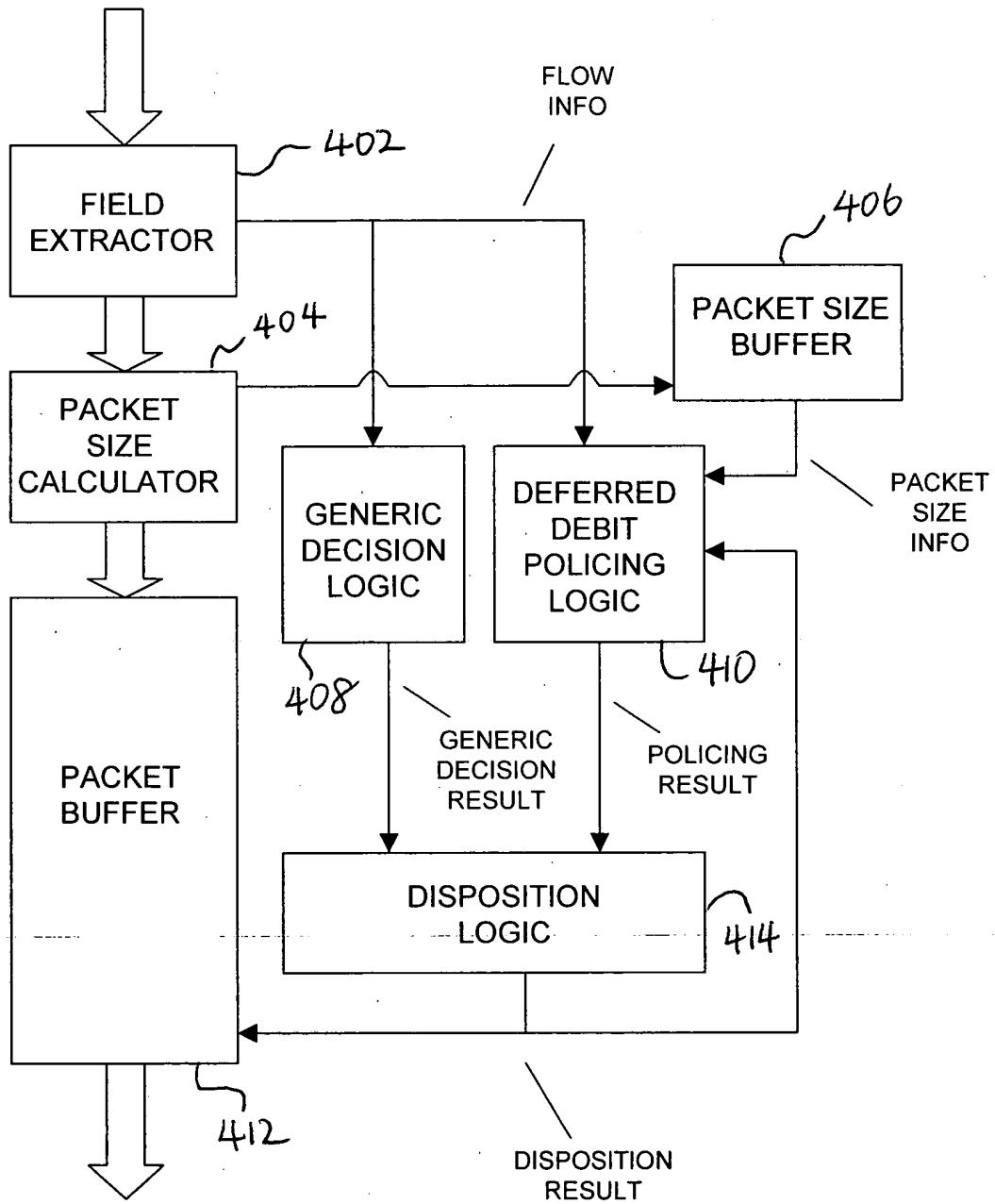
In the specification:

The paragraph beginning on page 22, line 31 has been amended as follows:

In one embodiment of the present invention, deferred debiting preferably is used with flow rate policing. FIG. 9 is a block diagram 400 of a packet switching controller having flow rate policing with deferred debiting in this embodiment of the present invention. The deferred debiting may be used in conjunction with the multi-level policing logic.

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